



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

7
Amclt
B
3-27-02
ary

In re the Application of:

Masahito ISODA

Art Unit: 2816

Application No.: 09/811,456

Examiner: L. Nguyen

Filed: March 20, 2001

Docket No.: 108075-00056

For: INPUT BUFFER CIRCUIT FOR SEMICONDUCTOR DEVICE

RESPONSE UNDER 37 CFR §1.121

Commissioner for Patents
Washington, D.C. 20231

March 15, 2002

Sir:

In response to the outstanding Office Action dated October 19, 2001, the time for reply having been extended to March 19, 2002 by the attached petition, please amend the application as follows:

IN THE DRAWINGS:

A corrected Fig. 6 is submitted herewith with changes annotated in red ink. In particular, text labels have been added, reference numeral "4" has been changed to --4A--, and labels "SW1" and "SW2" have been added.

IN THE SPECIFICATION:

Please amend the specification as follows:

Please replace page 14, paragraph 2 with:

RECEIVED
MAR 25 2002
TECHNOLOGY CENTER 2800

B